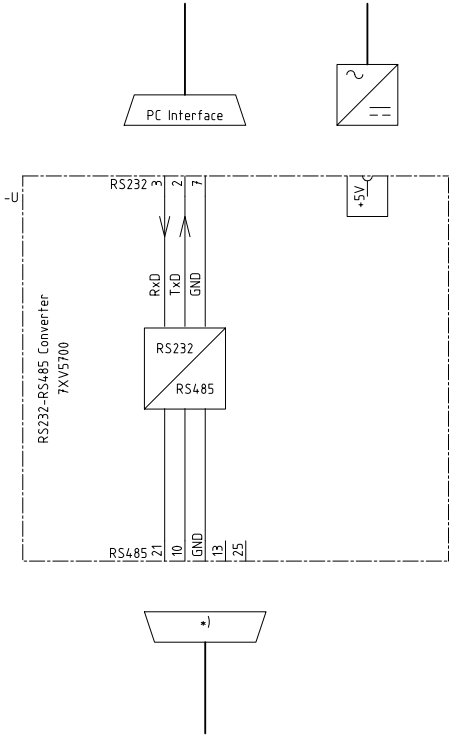


*) Plug
A: 21, 22, 23 jumped
B: 10, 11, 19 jumped
13, 25 jumped

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Archiv: BMP / 7XV5700 / S / 1 /

Projekt: siprotec
Symbolbibliothek 1: EVS6477
Symbolbibliothek 2: MS_40900
Symbolbibliothek 3:
Symbolbibliothek 4:

ELCAD-Version:
Translaredatei A: LEERA
Translaredatei B: LEERB
Translaredatei C: FB_E, 24.04.98
Translaredatei D:

| | | | | | | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|
| | | | | Date | 18.10.00 | EVS CAD-Pool | Siemens AG | CONVERTER 7XV5700 | | = | |
| | | | | Drawn | Bertrand | | | | S | + | 1 |
| | | | | Appr. | Böckenfeld | | | | - | | Sheet 1 |
| Rev. | Remarks | Date | Name | Norm | | Orig./Repl.f./Repl.by | | Circuit diagram | | | Sh. |