



UTC Demand Reply Bits

9th Jul 2010

Product: All Controller Firmware**Modifications Req:** No**Retrospective Action Req:** No**Priority of Change:** Information only

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Introduction

This bulletin details a workaround for an issue found on UTC Demand Reply bits where, if the same bit is used for two stages, that single reply bit will not work.

Scope

All Controller firmware.

Procedure

A UTC Demand Reply Bit indicates to the UTC Instation that there is a demand for the specified stage, or one of the phases in that stage.

These bits are configured on the IC4 screen "UTC Stage and Modes Data Definitions". This screen allows the names of UTC Reply Bits to be entered against each of the controller's stages.

However, if the name of the same UTC Reply Bit is entered against more than one stage, that UTC Reply Bit will not operate correctly. Therefore, a UTC Reply Bit must not be assigned to more than one function*.

In general, any controller output should not be assigned to more than one function. If an output is assigned to more than one function, those functions effectively 'fight' for control of the output, so the state of the output can not be guaranteed.

If it is required that an output combines the states of more than one facility, use Special Conditioning to read those states, combine them as required (e.g. AND or OR) and control the output.

* The issue has less impact with UTC Stage **Green Confirm** Bits, i.e. the reply bits that indicate a stage is at ROW. In existing configurations where the same UTC Reply Bit has been specified against more than one stage, the controller will operate correctly if those two stages cannot gain ROW at the same time (which is the rule if they appear in the same stream). However, for new configurations or updates, it is recommended that the configuration is corrected and Special Conditioning used to combine those functions in to one reply bit.

Related documents

Mantis Report 0009393

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