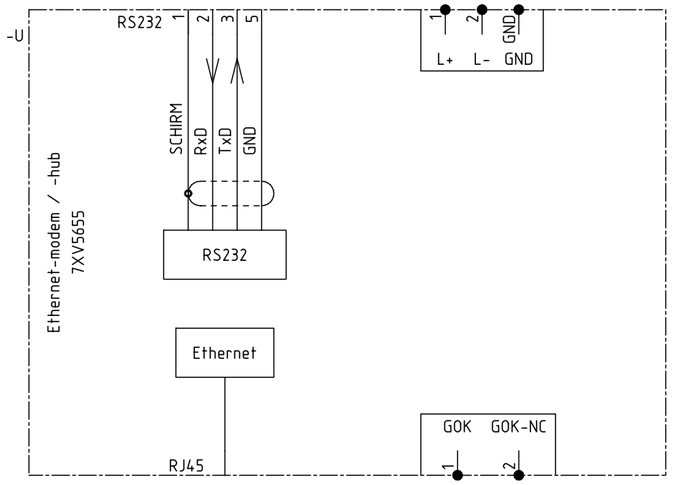

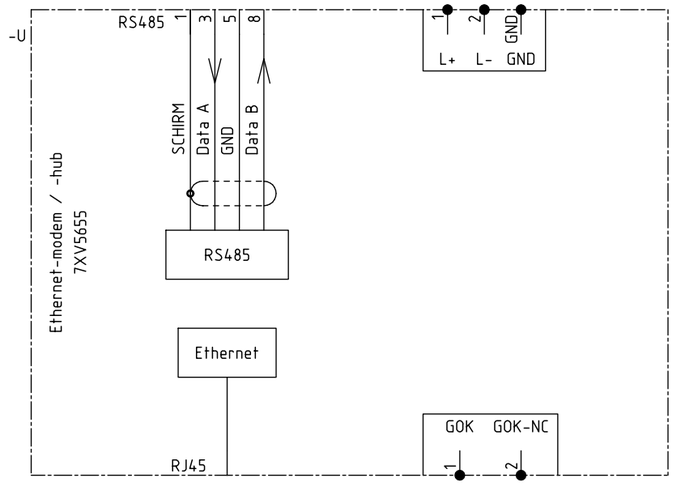

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Projekt: 7XV56xx_en
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Translatedatei D:

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Rev.	Remarks	Date	Name	Norm		Orig./Repl.f./Repl.by		Circuit diagram		1 Sh.