# SIEMENS



Manual



### S7-1500R/H

CPU 1515R-2 PN (6ES7515-2RM00-0AB0)

Edition

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# SIEMENS

PrefaceDocumentation guide1Product overview2Connecting3Interrupts, diagnostics, error<br/>messages and system<br/>events4Technical specifications5Dimension drawingA

# SIMATIC

### S7-1500R/H CPU 1515R-2 PN (6ES7515-2RM00-0AB0)

Manual

### Legal information

### Warning notice system

This manual contains notices you have to observe in order to ensure your personal safety, as well as to prevent damage to property. The notices referring to your personal safety are highlighted in the manual by a safety alert symbol, notices referring only to property damage have no safety alert symbol. These notices shown below are graded according to the degree of danger.

### 

indicates that death or severe personal injury will result if proper precautions are not taken.

### 

indicates that death or severe personal injury **may** result if proper precautions are not taken.

### 

indicates that minor personal injury can result if proper precautions are not taken.

### NOTICE

indicates that property damage can result if proper precautions are not taken.

If more than one degree of danger is present, the warning notice representing the highest degree of danger will be used. A notice warning of injury to persons with a safety alert symbol may also include a warning relating to property damage.

### **Qualified Personnel**

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### Preface

### Purpose of the documentation

This manual supplements the system manual of the S7-1500R/H redundant system and the function manuals. This manual contains a description of the module-specific information. The system-related functions are described in the system manual. All system-spanning functions are described in the function manuals.

The information provided in this manual and the system manual enables you to commission the CPU 1515R-2 PN.

### Conventions

STEP 7: In this documentation, "STEP 7" is used as a synonym for all versions of the configuration and programming software "STEP 7 (TIA Portal)".

Please also observe notes marked as follows:

#### Note

A note contains important information on the product described in the documentation, on the handling of the product or on the section of the documentation to which particular attention should be paid.

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### Industry Mall

The Industry Mall is the catalog and order system of Siemens AG for automation and drive solutions on the basis of Totally Integrated Automation (TIA) and Totally Integrated Power (TIP).

You can find catalogs for all automation and drive products on the Internet (https://mall.industry.siemens.com).

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# **Documentation guide**

The documentation for the redundant S7-1500R/H system is divided into three areas. This division enables you to access the specific content you require.

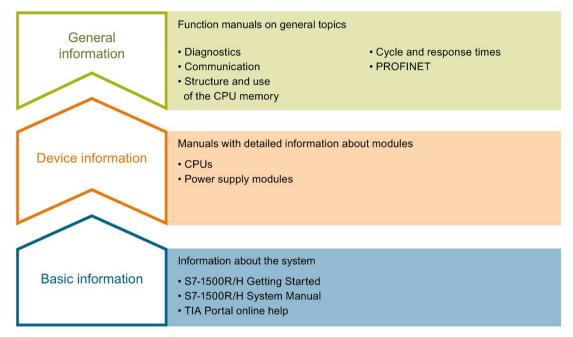


Figure 1-1 S7-1500R/H guide

#### **Basic information**

The System Manual and Getting Started describe in detail the configuration, installation, wiring and commissioning of the redundant S7-1500R/H system. The STEP 7 online help supports you in the configuration and programming.

#### **Device information**

Product manuals contain a compact description of the module-specific information, such as properties, wiring diagrams, characteristics and technical specifications.

#### **General information**

The function manuals contain detailed descriptions on general topics regarding the redundant S7-1500R/H system, e.g. diagnostics, communication.

You can download the documentation free of charge from the Internet (https://support.industry.siemens.com/cs/ww/en/view/109742691).

Changes and supplements to the manuals are documented in a Product Information.

You can download the product information free of charge from the Internet (https://support.industry.siemens.com/cs/ww/en/view/109742691).

### S7-1500/ET 200MP Manual Collection

The S7-1500/ET 200MP Manual Collection contains the complete documentation on the redundant S7-1500R/H system gathered together in one file.

You can find the Manual Collection on the Internet (https://support.industry.siemens.com/cs/ww/en/view/86140384).

### SIMATIC S7-1500 comparison list for programming languages

The comparison list contains an overview of which instructions and functions you can use for which controller families.

You can find the comparison list on the Internet (https://support.industry.siemens.com/cs/ww/en/view/86630375).

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You configure your own download package with a few clicks.

In doing so you can select:

- Product images, 2D dimension drawings, 3D models, internal circuit diagrams, EPLAN macro files
- Manuals, characteristics, operating manuals, certificates
- Product master data

You can find "mySupport" - CAx data on the Internet (https://support.industry.siemens.com/my/ww/en/CAxOnline).

### **Application examples**

The application examples support you with various tools and examples for solving your automation tasks. Solutions are shown in interplay with multiple components in the system - separated from the focus on individual products.

You will find the application examples on the Internet (https://support.industry.siemens.com/sc/ww/en/sc/2054).

### PRONETA

With SIEMENS PRONETA (PROFINET network analysis), you analyze the PROFINET network during commissioning. PRONETA features two core functions:

- The topology overview independently scans PROFINET network and all connected components.
- The IO check is a fast test of the wiring and the module configuration of a system.

You can find SIEMENS PRONETA on the Internet (https://support.industry.siemens.com/cs/ww/en/view/67460624).

### SINETPLAN

SINETPLAN, the Siemens Network Planner, supports you in planning automation systems and networks based on PROFINET. The tool facilitates professional and predictive dimensioning of your PROFINET installation as early as in the planning stage. In addition, SINETPLAN supports you during network optimization and helps you to exploit network resources optimally and to plan reserves. This helps to prevent problems in commissioning or failures during productive operation even in advance of a planned operation. This increases the availability of the production plant and helps improve operational safety.

The advantages at a glance

- · Network optimization thanks to port-specific calculation of the network load
- · Increased production availability thanks to online scan and verification of existing systems
- Transparency before commissioning through importing and simulation of existing STEP 7 projects
- Efficiency through securing existing investments in the long term and optimal exploitation of resources

You can find SINETPLAN on the Internet (https://www.siemens.com/sinetplan).

# **Product overview**

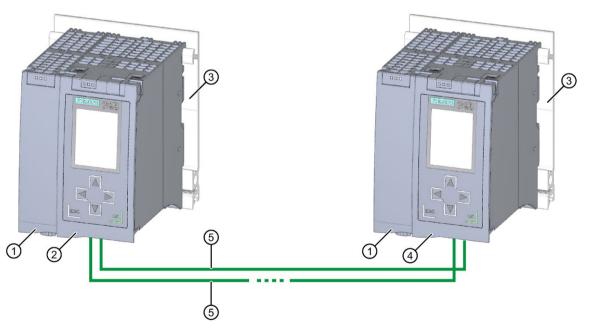
### 2.1 Configuration and operating principle

### Structure

The S7-1500R redundant system consists of the following components:

- Two CPUs of the type CPU 1515R-2 PN
- Two SIMATIC memory cards
- PROFINET cable (PROFINET ring)
- IO devices
- Load power supply (optional)
- System power supply (optional)

You mount the CPUs on a common mounting rail or spatially separated on two separate mounting rails. You connect the two CPUs and the IO devices in a PROFINET ring via the PROFINET cable.



- ① Optional load current supply
- ② First CPU
- ③ Mounting rail with integrated DIN rail profile
- ④ Second CPU
- 5 PROFINET cable (PROFINET ring)
- Figure 2-1 Configuration example for S7-1500R

### Principle of operation

One of the two CPUs in the redundant system takes on the role of CPU for process control (primary CPU). The other CPU takes on the role of the following CPU (backup CPU). The assigned role of the CPUs can change during operation. Synchronization of all relevant data between primary CPU and backup CPU ensures fast switching between CPUs in the event of a primary CPU failure. If the primary CPU fails, the backup CPU retains control of the process as the new primary CPU at the point of interruption.

The redundancy connections are the PROFINET ring with MRP. The CPUs are synchronized via a PROFINET ring.

### Reference

You can find a detailed description of the operation and design of the CPUs in the system manual Redundant System S7-1500R/H (https://support.industry.siemens.com/cs/ww/en/view/109754833).

2.2 Hardware properties

### 2.2 Hardware properties

### Article number

6ES7515-2RM00-0AB0

### View of the module

The figure below shows the CPU 1515R-2 PN.



Figure 2-2 CPU 1515R-2 PN

### Note

### Protective film

Note that there is a removable protective foil on the display when the CPUs are delivered.

### Properties

Property	Description	Additional information
CPU display	All CPUs of the redundant system S7 1500R/H have a display with plain text information. The display provides you with diagnostic messages as well as information about the article number, the firmware version and the serial number of the CPU. You can also view and assign the IP addresses, the PROFINET device name and the redundancy ID of the CPU. The system IP address can be viewed via STEP 7 but not in the display. In addition to the functions listed here, a large number of other functions are available on the display. These additional functions are described in the SIMATIC S7 1500 Display Simulator.	<ul> <li>Redundant System S7-1500R/H (https://support.industry.siemens. com/cs/ww/en/view/109754833) System Manual</li> <li>SIMATIC S7-1500 Display Simulator (http://www.automation.siemens. com/salesmaterial-as/interactive- manuals/getting-started_simatic- s7-1500/disp_tool/start_de.html)</li> </ul>
Supply voltage         The 24 V DC supply voltage is fed via a 4-pin plug located on the front of the CPU.		<ul> <li>Section Connecting (Page 22)</li> <li>Redundant System S7-1500R/H (<u>https://support.industry.siemens.</u> <u>com/cs/ww/en/view/109754833</u>) System Manual</li> </ul>

### CPU 1515R-2 PN has the following technical properties:

2.2 Hardware properties

Property	Description	Additional information		
PROFINET IO				
PROFINET IO interface (X1 P1 R and X1 P2 R)	<ul> <li>The CPU has an X1 interface with two ports (X1 P1 R and X1 P2 R).</li> <li>The PROFINET IO interface X1 (default P1 R) is used to set up the PROFINET ring with the two CPUs and the IO devices.</li> <li>The PROFINET IO interface X1 (default P2 R) is used to make the connection between the two R-CPUs in the PROFINET ring.</li> </ul>	<ul> <li>Redundant System S7-1500R/H (https://support.industry.siemens. com/cs/ww/en/view/109754833) System Manual</li> <li>Function manual PROFINET (https://support.industry.siemens. com/cs/ww/en/view/49948856)</li> </ul>		
	<ul> <li>In the PROFINET ring, the synchronization frames between the CPUs are transmitted via the following connections: <ul> <li>The direct connection (X1 P2 R)</li> <li>The indirect connection (X1 P1 R) via the IO devices</li> </ul> </li> <li>The interface supports PROFINET IO RT (Real-Time) and PROFINET basic functionality. <ul> <li>Basic PROFINET functionality comprises:</li> <li>HMI communication</li> <li>Communication with the configuration system</li> <li>Communication with a higher-level network (backbone, router, Internet)</li> <li>Communication with another machine or automation cell</li> </ul> </li> </ul>			
PROFINET interface (X2 P1)	The CPU has an X2 interface with one port (X2 P1). The interface supports PROFINET basic functionality.			
Operation of the CPUs as IO controllers	IO controller: As IO controllers the CPUs address the following con- figured IO devices:			
	<ul> <li>IO devices within the PROFINET ring</li> <li>IO devices that are decoupled from the PROFINET ring via a switch</li> </ul>			

### Accessories

You can find information on the topic of "Accessories/spare parts" in the system manual for Redundant System S7-1500R/H (https://support.industry.siemens.com/cs/ww/en/view/109754833).

### 2.3 Firmware functions

### Functions

### CPU 1515R-2 PN supports the following firmware functions:

Function	Description	Additional information
CPU redundancy	There are two duplicate CPUs that synchronize their data via redundancy connections within a PROFINET ring. If one of the CPUs fails, the other CPU retains control of the process.	Redundant System S7-1500R/H (https://support.industry.siemens.co m/cs/ww/en/view/109754833) Sys- tem Manual
Integrated system diag- nostics	The system automatically generates the messages for the system diagnostics and outputs these messages via a programming device/PC, HMI device or the inte- grated display. System diagnostics information is also available when the CPUs are in operating state STOP.	Function manual Diagnostics (http://support.automation.siemens.c om/WW/view/en/59192926)
Integrated trace function- ality PROFINET IO	Trace functionality supports you in troubleshooting and/or optimizing the user program. You record device tags and evaluate the recordings with the trace and logic analyzer function. Tags are, for example, drive parameters or system and user tags of a CPU. Trace and logic analyzer functions are suitable for monitoring highly dynamic processes. Note: Note that the S7-1500R/H redundant system supports recording of measurements. However, saving the measurements to the SIMATIC memory card is not supported.	Function manual Using the trace and logic analyzer function (http://support.automation.siemens.c om/WW/view/en/64897128)
System redundancy S2	All IO devices are connected redundantly in the redun- dant S7 1500R/H system. All IO devices assigned to the system must therefore support system redundancy S2. If the role of the CPUs changes, the new primary CPU takes over the PROFINET IO communication.	<ul> <li>Redundant System S7-1500R/H (https://support.industry.siemens. com/cs/ww/en/view/109754833) System Manual</li> <li>Function manual PROFINET (http://support.automation.sieme ns.com/WW/view/en/49948856)</li> </ul>
RT (real time)	RT prioritizes PROFINET IO frames over standard frames. This ensures the required determinism in the automation technology. In this process the data is transferred via prioritized Ethernet frames.	Function manual PROFINET (http://support.automation.siemens.c om/WW/view/en/49948856)
MRP (Media Redundancy Protocol)	The Media Redundancy Protocol enables the configu- ration of redundant networks. Redundant transmission links (ring topology) ensure that an alternative commu- nication path is made available if a transmission link fails. Within the PROFINET ring, the R-CPUs assume the role of the MRP Manager following appropriate project configuration and all other devices in the ring assume the role of the MRP clients.	

### Product overview

### 2.3 Firmware functions

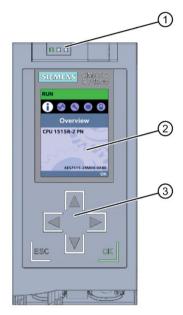
Function	Description	Additional information
PROFlenergy	PROFIenergy is a PROFINET-based data interface for switching off consumers centrally and with full coordi- nation during pause times regardless of the manufac- turer or device type. Through this, the process should only be provided with the energy that is absolutely required. Most of the energy is saved by the process. The PROFINET device itself only contributes a few watts to the savings potential.	
Integrated technology		
Integrated closed-loop control functionality	The CPUs support PID basic functions. No controller support: • PID_Compact • PID_3Step • PID_Temp	Function manual PID Control ( <u>https://support.industry.siemens.co</u> m/cs/ww/en/view/108210036)
Security Integrated		
Know-how protection	The know-how protection protects user blocks against unauthorized access and modifications.	Redundant System S7-1500R/H (https://support.industry.siemens.co
Access protection	You can use authorization levels to assign separate rights to different user groups.	m/cs/ww/en/view/109754833) Sys- tem Manual
Integrity protection	The CPUs dispose of integrity protection by default. Integrity protection identifies possible manipulations of engineering data on the SIMATIC memory card or during data transfer between STEP 7 and the CPUs.	
	Integrity protection also checks the communication from a SIMATIC HMI system to the CPUs for possible manipulations of engineering data.	
	If integrity protection identifies the manipulation of en- gineering data, the user receives a corresponding mes- sage.	
Password provider	As an alternative to manual password entry, you can link a password provider to STEP 7. A password pro- vider offers the following advantages:	
	<ul> <li>Convenient handling of passwords. STEP 7 auto- matically imports the password for the blocks. This saves you time.</li> </ul>	
	Optimum block protection because the users do not know the password itself.	

2.4 Operator controls and display elements

### 2.4 Operator controls and display elements

### 2.4.1 Front view of the module with front panel

The figure below shows the front view of the CPU 1515R-2 PN.



- ① LEDs for the current operating state and diagnostic status of the CPU
- ② Display
- ③ Control keys

Figure 2-3 View of the CPU 1515R-2 PN (with front panel) - front

### Note

#### Temperature range for display

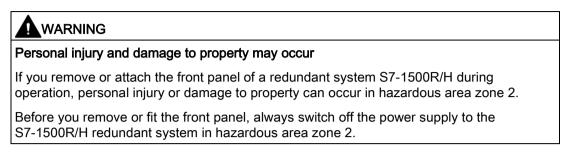
To increase its service life, the display switches off at a temperature below the permitted operating temperature of the device. When the display cools down, it automatically switches itself on again. When the display is switched off, the LEDs continue to show the status of the CPUs.

You can find additional information on the temperatures at which the display switches itself on and off in the Technical specifications (Page 31).

2.4 Operator controls and display elements

### Pulling and plugging the front panel with display

You can pull and plug the front panel with display during operation.



### Locking the front panel

You can lock the front panel to protect the SIMATIC memory card and the mode selector of the CPU against unauthorized access.

You can attach a security seal or a padlock with a hoop diameter of 3 mm to the front panel.



Figure 2-4 Locking latch on the CPU

In addition to the mechanical lock, you can also block access to a password-protected CPU on the display (local lock) and assign a password for the display. You can find additional information on the display, the configurable protection levels and the local lock in the system manual for Redundant System S7-1500R/H

(https://support.industry.siemens.com/cs/ww/en/view/109754833).

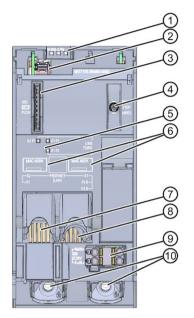
### Reference

You can find detailed information on the individual display options, a training course and a simulation of the available menu commands in the SIMATIC S7-1500 Display Simulator (<u>http://www.automation.siemens.com/salesmaterial-as/interactive-manuals/getting-</u>started\_simatic-s7-1500/disp\_tool/start\_en.html).

2.4 Operator controls and display elements

### 2.4.2 Front view of the module without front panel

The figure below shows the operator controls and connection elements of the CPU 1515R-2 PN.



- ① LEDs for the current operating state and diagnostic status of the CPUs
- ② Display connector
- ③ Slot for the SIMATIC memory card
- ④ Mode selector
- (5) LED displays for the 3 ports of the PROFINET interfaces X1 and X2
- 6 MAC addresses of the interfaces
- PROFINET IO interface X2 with 1 port
- 8 PROFINET IO interface X1 with 2 ports
- (9) Connector for power supply
- Fixing screws

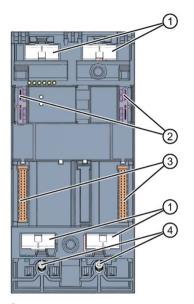
Figure 2-5 View of the CPU 1515R-2 PN (without front panel) - front

### Product overview

2.4 Operator controls and display elements

### 2.4.3 Rear view of the module

The figure below shows the connection elements on the rear of the CPU 1515R-2 PN.



- ① Shield contact surfaces
- 2 Plug-in connection for power supply
- ③ Plug-in connection for backplane bus
- ④ Fixing screws

Figure 2-6 View of the CPU 1515R-2 PN - rear

### 2.5 Mode selector

You use the mode selector to:

- Request a change to a specific operating state
- Disable or enable the change of a specific operating state

(if, for example, the mode selector is set to STOP, you cannot switch the CPU to RUN via a communication task or the display)

The following table shows the position of the switch and the corresponding meaning.

Table 2-1 Mode switch settings

Position	Meaning	Explanation
RUN	RUN mode	The CPU has permission to go to RUN.
STOP	STOP mode	The CPU does not have permission to go to RUN.
MRES	Memory reset	Position for CPU memory reset.

### Reference

You can find a brief overview of the various operating states and system states in the section Status and error display of the CPU (Page 26).

You can find a detailed description of the operating states and system states in the system manual for S7-1500R/H Redundant System (https://support.industry.siemens.com/cs/ww/en/view/109754833).

# Connecting

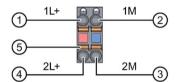
### 3.1 Terminal assignment

This section provides information on the terminal assignment of the individual interfaces and the block diagram of the CPU 1515R-2 PN.

### 24 V DC supply voltage (X80)

The connector for the power supply is plugged in when the CPU ships from the factory.

The following table shows the terminal assignment for a 24 V DC power supply.



- (1) +24 V DC of the supply voltage
- ② Ground of the supply voltage
- ③ Ground of the supply voltage for loop-through (maximum of 10 A permitted)
- 4 +24 V DC of the supply voltage for loop-through (maximum of 10 A permitted)
- 5 Spring opener (one spring opener per terminal)

Bridged internally:

(1) and (4)

(2) and (3)

Figure 3-1 Supply voltage connection

You can find information on the various supply options in the system manual for Redundant System S7-1500R/H (https://support.industry.siemens.com/cs/ww/en/view/109754833).

### PROFINET interface X1 with 2-port switch (X1 P1 R and X1 P2 R)

The assignment corresponds to the Ethernet standard for a RJ45 connector.

- When autonegotiation is deactivated, the RJ45 socket is allocated as a switch (MDI-X).
- When autonegotiation is activated, autocrossing is in effect and the RJ45 socket is allocated either as data terminal equipment (MDI) or a switch (MDI-X).

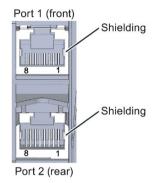


Figure 3-2 Interface assignments

### PROFINET interface X2 with 1 port (X2 P1)

The assignment corresponds to the Ethernet standard for a RJ45 connector.

Autocrossing is always active on X2. This means the RJ45 socket is allocated either as data terminal equipment (MDI) or a switch (MDI-X).

### Reference

You can find additional information on the topic of "Connecting the CPU" and on the topic "Accessories/spare parts" in the system manual for Redundant System S7-1500R/H (https://support.industry.siemens.com/cs/ww/en/view/109754833).

3.1 Terminal assignment

### Assignment of the MAC addresses

For each CPU, CPU 1515R-2 PN has:

- One PROFINET interface with two ports
- One PROFINET interface with one port

Each of the PROFINET interfaces has a MAC address and each of the PROFINET ports has its own MAC address. There are a total of ten MAC addresses for the two CPUs of the CPU 1515R-2 PN.

The MAC addresses of the PROFINET ports are needed for the LLDP protocol, for example for the neighborhood discovery function.

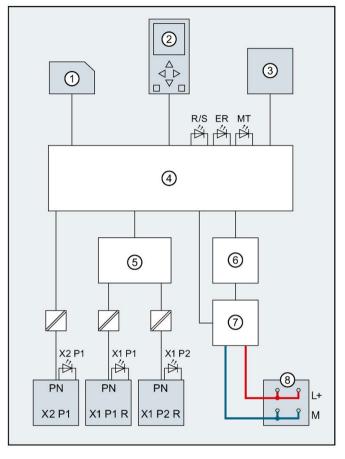
The number range of the MAC addresses is sequential. The first and last MAC addresses are printed on the rating plate on the right side of each CPU 1515R-2 PN.

The table below shows how the MAC addresses are assigned.

Table 3-1 Assignment of MAC addresses using the example of a single CPU

	Assignment	Labeling
MAC address 1	PROFINET interface X1 (visible in STEP 7 for accessible devices)	<ul> <li>Front printed</li> <li>Right-side printed (start of number range)</li> </ul>
MAC address 2	Port X1 P1 R (required for LLDP, for example)	
MAC address 3	Port X1 P2 R (required for LLDP, for example)	
MAC address 4	PROFINET interface X2 (visible in STEP 7 for accessible devices)	Front printed
MAC address 5	Port X2 P1 (required for LLDP, for example)	Right-side printed     (end of number range)

### Block diagram



The following figure shows the block diagram of the CPU 1515R-2 PN.

1	SIMATIC memory card (X50)	PN X1 P2 R	PROFINET interface X1 port 2
2	Display	PN X2 P1	PROFINET interface X2 port 1
3	Mode selector RUN/STOP/MRES	L+	24 V DC supply voltage
4	Electronics	Μ	Ground
5	PROFINET 2-port switch	R/S	RUN/STOP LED (yellow/green)
6	Backplane bus connection (connection to backplane bus not configurable)	ER	ERROR LED (red)
$\bigcirc$	Internal supply voltage	MT	MAINT LED (yellow)
8	Supply of the 24 V DC supply voltage (X80)	X1 P1, X1 P2, X2 P1	LED Link TX/RX
PN X1 P1 R	PROFINET interface X1 port 1		

Figure 3-3 Block diagram of the CPU 1515R-2 PN

# Interrupts, diagnostics, error messages and system events

### 4.1 Status and error display of the CPU

The LED displays of the CPUs are described below.

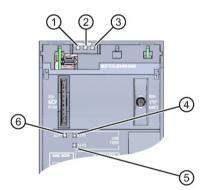
You can find more detailed information on "Interrupts" in the STEP 7 online help.

You can find additional information on the topic of "Diagnostics" and "System events" in the Diagnostics (<u>http://support.automation.siemens.com/WW/view/en/59192926</u>) function manual and in the system manual Redundant System S7-1500R/H (<u>https://support.industry.siemens.com/cs/ww/en/view/109754833</u>).

You can find additional information on the topic of "Operating states and system states" as well as various failure scenarios in the system manual for S7-1500R/H Redundant System (https://support.industry.siemens.com/cs/ww/en/view/109754833).

### LED display

The figure below shows the LED displays of the CPU 1515R-2 PN.



- 1 RUN/STOP LED (yellow/green LED)
- ② ERROR LED (red LED)
- ③ MAINT LED (yellow LED)
- ④ LINK RX/TX LED for port X1 P1 (yellow/green LED)
- 5 LINK RX/TX LED for port X1 P2 (yellow/green LED)
- 6 LINK RX/TX LED for port X2 P1 (yellow/green LED)

Figure 4-1 LED display of the CPU 1515R-2 PN (without front panel)

### LED displays depending on operating states and system states

CPU 1515R-2 PN has the following LEDs for displaying the current operating state and diagnostics status.

- RUN/STOP LED
- ERROR LED
- MAINT LED

The LEDs indicate the operating state of the respective CPU within the redundant system. Operating states describe the behavior of a single CPU at a specific time. The combination of the operating states of the CPUs forms the system state.

The following figure shows the possible operating states of the CPUs and the resulting system states.

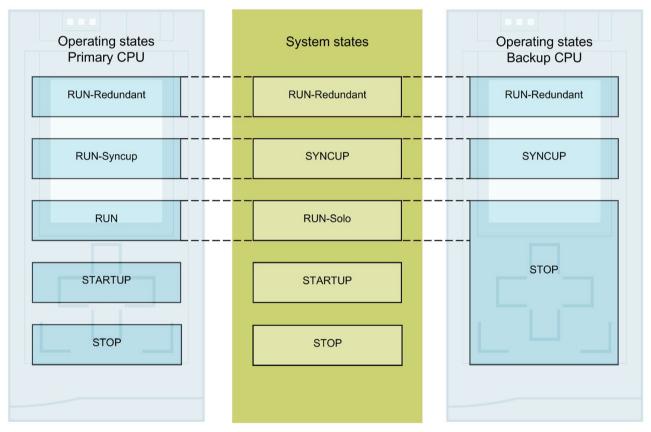


Figure 4-2 Operating states and system states

### Meaning of the RUN/STOP, ERROR and MAINT LEDs

CPU 1515R-2 PN has the following LEDs for displaying the current operating state and diagnostics status.

#### Note

### LED patterns of the redundant system S7 1500R

Note that it is not always possible to:

- Determine the state of the CPU from the signal pattern
- Determine the state of the other CPU from the signal pattern

The "Meaning" column only shows a possible typical cause.

To investigate the cause of the signal pattern, use the diagnostic buffer and its display via:

- STEP 7
- HMI devices
- Displays of the CPUs

The following table shows the meaning of the various color combinations for the RUN/STOP, ERROR and MAINT LEDs.

RUN/STOP LED	ERROR LED	MAINT LED	Meaning
LED off	LED off	LED off	Missing or insufficient supply voltage on the CPU.
	送	送	Startup (CPU booting)
LED flashes	LED flashes red	LED flashes yellow	Test of LEDs during startup, inserting a module.
yellow/green			LED flashing test
-		-	CPU is in operating state STOP.
LED lit yellow	LED off	LED lit yellow	Completion of system initialization
₩ LED flashes yellow	LED off	LED lit yellow	CPU executes internal activities in an operating state ≠ RUN-Redundant.
	渋	送	CPU defective
LED lit yellow	LED flashes red	LED flashes yellow	
-		送	Firmware update successfully completed.
LED lit yellow	LED off	LED flashes yellow	
LED flashes yellow/green	LED off	LED lit yellow	The primary CPU is in operating state STARTUP.
LED flashes yellow/green	上ED flashes red	LED lit yellow	The backup CPU is in operating state SYNCUP. The backup CPU has not yet been restarted for SYNCUP during this phase.
上ED flashes yellow	LED off	LED off	The CPU performs a warm restart.
LED lit green	LED off	LED lit yellow	Maintenance demanded for the plant. You need to check/replace the affected hardware within a short period of time.
			The primary CPU is in operating state RUN- Syncup.
			Active Force job
			PROFlenergy pause
			The primary CPU is in operating state RUN.
			The CPU is in operating state RUN-Redundant.
LED lit green	LED off	LED off	There are no events, requirements, errors, etc.
LED lit green	上ED flashes red	LED off	A diagnostic event is pending in operating state RUN-Redundant.
LED lit green	LED flashes red	LED lit yellow	A diagnostic event (e.g. failure of an IO device within the PROFINET ring or no access to SIMATIC memory card possible <sup>1)</sup> ) and mainte- nance is demanded (e.g. interruption of the PROFINET ring).

<sup>1)</sup> If access to the SIMATIC memory card is not possible in RUN-Redundant (wrong card, card full/write protected), the system switches to RUN-Solo. The ERROR LED flashes three times. The MAINT LED lights up until the RUN-Redundant system status is reached again.

### Note MAINT LED of the two CPUs

The MAINT LEDs of both CPUs only go out when the following conditions are fulfilled:

- The CPUs are in the RUN-Redundant system state.
- No maintenance is demanded.

#### Note

### ERROR LED of the backup CPU

In non-redundant operation, the backup CPU does not establish a connection to the IO devices. Therefore, in addition to the LED patterns shown in the "Meaning of the LEDs" table, note that the ERROR LED of the backup CPU always flashes red in this state.

### Note

### LED displays in redundant mode

In the RUN-Redundant system state, the LED displays on both CPUs are identical.

### Meaning of LINK RX/TX LED

Each port has a LINK RX/TX LED. The table below shows the various LED patterns of the ports of the CPU 1515R-2 PN.

Table 4- 2 Meaning of LINK RX/TX LED

LINK TX/RX LED	Meaning
□ off	There is no Ethernet connection between the PROFINET interface of the PROFINET device and the communication partner.
	No data is currently being sent/received via the PROFINET interface.
	There is no LINK connection.
	The redundancy connections were interrupted.
送	The CPU performs an LED flash test.
Flashes green	
Illuminated green	There is an Ethernet connection between the PROFINET interface of your PROFINET device and a communication partner.
<b>J</b>	The redundancy connections are OK.
上ED flashes	Data is currently being received/sent by a communication partner via the PROFINET inter- face of the PROFINET device.
yellow/green	

# **Technical specifications**

### General technical specifications

Article number	6ES7515-2RM00-0AB0
General information	
Product type designation	CPU 1515R-2 PN
HW functional status	FS01
Firmware version	V2.6
Engineering with	
STEP 7 TIA Portal configurable/integrated as of version	STEP 7 V15.1 or higher
Display	
Screen diagonal [cm]	6.1 cm
Control elements	
Number of keys	6
Mode selector switch	1
Supply voltage	
Type of supply voltage	24 V DC
permissible range, lower limit (DC)	19.2 V
permissible range, upper limit (DC)	28.8 V
Reverse polarity protection	Yes
Mains buffering	
Mains/voltage failure stored energy time	5 ms
Input current	
Current consumption (rated value)	0.8 A
Inrush current, max.	2.4 A
²t	0.02 A <sup>2</sup> ·s
Power loss	
Power loss, typ.	6.3 W
Memory	
Number of slots for SIMATIC memory card	1
SIMATIC memory card required	Yes
Work memory	
<ul> <li>integrated (for program)</li> </ul>	500 kbyte
• integrated (for data)	3 Mbyte
Load memory	
• Plug-in (SIMATIC Memory Card), max.	32 Gbyte

Article number	6ES7515-2RM00-0AB0
Backup	
maintenance-free	Yes
CPU processing times	
for bit operations, typ.	60 ns
for word operations, typ.	72 ns
for fixed point arithmetic, typ.	96 ns
for floating point arithmetic, typ.	384 ns
CPU-blocks	6 000: Blocks (OB, EB, EC, DB) and LIDTs
Number of elements (total)	6 000; Blocks (OB, FB, FC, DB) and UDTs
Number range	Number range: 1 to 59 999
• Size, max.	3 Mbyte; For non-optimized block accesses, the max. size of the DB is 64 KB
FB	
Number range	0 65 535
• Size, max.	500 kbyte
FC	
Number range	0 65 535
• Size, max.	500 kbyte
ОВ	
• Size, max.	500 kbyte
Number of free cycle OBs	100
Number of time alarm OBs	20
Number of delay alarm OBs	20
Number of cyclic interrupt OBs	20
Number of process alarm OBs	50
Number of startup OBs	100
Number of asynchronous error OBs	4
Number of synchronous error OBs	2
Number of diagnostic alarm OBs	1
Nesting depth	
per priority class	24
Counters, timers and their retentivity	
S7 counter	
Number	2 048
Retentivity	
– adjustable	Yes

IEC counter       Any (only limited by the main memory)         Retentivity       - adjustable         - adjustable       Yes         S7 times       2 048         Retentivity       - adjustable         - adjustable       Yes         EEC timer       Any (only limited by the main memory)         Retentivity       - adjustable         - adjustable       Yes         EEC timer       Any (only limited by the main memory)         Retentivity       - adjustable         - adjustable       Yes         Data areas and their rotentivity       Flag         Number, max.       16 kbyte         Number of clock memories       8: 8 clock memory bit, grouped into one clock memory byte         Data blocks       Yes         Retentivity adjustable       Yes         Retentivity preset       No         Local data       64 kbyte; max. 16 KB per block         Address area       Number of IO modules         Vo address area       32 kbyte; All inputs are in the process image         per integrated IO subsystem       -         - Inputs (volume)       8 kbyte         - Outputs (volume)       8 kbyte         Subprocess images       32         Number of I	Article number	6ES7515-2RM00-0AB0
Retentivity       - adjustable       Yes         97 times       2 048         Retentivity       - adjustable         - adjustable       Yes         EEC timer       Any (only limited by the main memory)         Retentivity       - adjustable         - adjustable       Yes         Data areas and their retentivity       Any (only limited by the main memory)         Retentivity       - adjustable         - adjustable       Yes         Data areas and their retentivity       S12 kbyte         Retentive data area (incl. timers, counters, flags), max.       512 kbyte         Is Number, max.       16 kbyte         Number of clock memories       8; 8 clock memory bit, grouped into one clock memory byte         Data blocks       Yes         Retentivity adjustable       Yes         Retentivity adjustable       Yes         Retentivity preset       No         Local data       64 kbyte; max. 16 KB per block         Address area       Number of IO modules         Number of IO modules       4 096; max. number of modules / submodules         I/O address area       32 kbyte; All inputs are in the process image         Outputs       32 kbyte; All outputs are in the process image         Outputs (volume	IEC counter	
- adjustable       Yes         S7 times       2 048         Retentivity       - adjustable         - adjustable       Yes         IEC timer       - Any (only limited by the main memory)         Retentivity       - adjustable         - adjustable       Yes         Data areas and their retentivity       - adjustable         - adjustable       Yes         Data areas and their retentivity       512 kbyte         Retentive data area (incl. timers, counters, flags), max.       512 kbyte         Flag       - Number of clock memories         • Number of clock memories       8; 8 clock memory bit, grouped into one clock memory byte         Data blocks       - Retentivity adjustable         • Retentivity preset       No         Local data       - per priority class, max.         • Per priority class, max.       64 kbyte; max. 16 KB per block         Address area       - Mumber of IO modules         /Vo address area       - Inputs         • Inputs       32 kbyte; All outputs are in the process image         • Outputs       32 kbyte; All outputs are in the process image         • Outputs (volume)       8 kbyte         - Outputs (volume)       8 kbyte         - Outputs (volume)       8 kbyte <td>Number</td> <td>Any (only limited by the main memory)</td>	Number	Any (only limited by the main memory)
S7 times       2 048         • Number       2 048         Retentivity       - adjustable         - adjustable       Yes         IEC timer       Any (only limited by the main memory)         Retentivity       - adjustable         - adjustable       Yes         Data areas and their retentivity       File         Retentive data area (incl. timers, counters, flags), max.       512 kbyte         Flag       512 kbyte         Number, max.       16 kbyte         Number of clock memories       8; 8 clock memory bit, grouped into one clock memory byte         Data blocks       -         • Retentivity adjustable       Yes         Local data       64 kbyte; max. 16 KB per block         Address area       -         Number of IO modules       4 096; max. number of modules / submodules         //O address area       -         • Inputs       32 kbyte; All inputs are in the process image         • Outputs       32 kbyte; All outputs are in the process image         • Outputs       8 kbyte         • Outputs (volume)       8 kbyte         • Outputs (volume)       8 kbyte         • Outputs (volume)       8 kbyte	Retentivity	
• Number       2 048         Retentivity       - adjustable         • Number       Any (only limited by the main memory)         Retentivity       - adjustable         • Number       Any (only limited by the main memory)         Retentivity       - adjustable         - adjustable       Yes         Data areas and their retentivity       512 kbyte         Retentive data area (incl. timers, counters, flags), max.       512 kbyte         • Number of clock memories       8; 8 clock memory bit, grouped into one clock memory byte         Data blocks       8; 8 clock memory byte         • Number of clock memories       8; 8 clock memory byte         Data blocks       Yes         • Retentivity adjustable       Yes         • Retentivity preset       No         Local data       64 kbyte; max. 16 KB per block         Address area       10 muber of IO modules         / O address area       32 kbyte; All inputs are in the process image         • Inputs       32 kbyte; All outputs are in the process image         • Outputs       8 kbyte         Subprocess images       32         • Number of IO controllers       8 kbyte	– adjustable	Yes
Retentivity       - adjustable       Yes         IEC timer       Any (only limited by the main memory)         Retentivity       - adjustable         - adjustable       Yes         Data areas and their retentivity       Flag         Retentive data area (incl. timers, counters, flags), max.       512 kbyte         Flag       -         • Number, max.       16 kbyte         • Number of clock memories       8; 8 clock memory bit, grouped into one clock memory byte         Data blocks       -         • Retentivity adjustable       Yes         • Retentivity preset       No         Local data       64 kbyte; max. 16 KB per block         Address area       4 096; max. number of modules / submodules         I/O address area       32 kbyte; All inputs are in the process image         • Inputs       32 kbyte; All outputs are in the process image         • Inputs       32 kbyte; All outputs are in the process image         Per integrated IO subsystem       8 kbyte         - Inputs (volume)       8 kbyte         Subprocess images       32         • Number of IO Controllers       32	S7 times	
- adjustable       Yes         IEC timer       Any (only limited by the main memory)         Retentivity       - adjustable         - adjustable       Yes         Data areas and their retentivity       File         Retentive data area (incl. timers, counters, flags), max.       512 kbyte         Flag       512 kbyte         • Number, max.       16 kbyte         • Number of clock memories       8; 8 clock memory bit, grouped into one clock memory byte         Data blocks       -         • Retentivity adjustable       Yes         • Retentivity preset       No         Local data       64 kbyte; max. 16 KB per block         Address area       32 kbyte; All inputs are in the process image         • Inputs       32 kbyte; All inputs are in the process image         • Outputs       32 kbyte; All outputs are in the process image         • Inputs       32 kbyte; All outputs are in the process image         • Inputs (volume)       8 kbyte         Subprocess images       32         • Number of subprocess images, max.       32	Number	2 048
IEC timer       Any (only limited by the main memory)         Retentivity       - adjustable         - adjustable       Yes         Data areas and their retentivity       512 kbyte         Retentive data area (incl. timers, counters, flags), max.       512 kbyte         Flag       .         • Number, max.       16 kbyte         • Number of clock memories       8; 8 clock memory bit, grouped into one clock memory byte         Data blocks       .         • Retentivity adjustable       Yes         • Retentivity preset       No         Local data       64 kbyte; max. 16 KB per block         Address area       .         • Inputs       32 kbyte; All inputs are in the process image         • Outputs       32 kbyte; All outputs are in the process image         • Outputs       8 kbyte         - Outputs (volume)       8 kbyte         Subprocess images       .         • Number of IO Controllers       32	Retentivity	
• Number       Any (only limited by the main memory)         Retentivity       - adjustable         - adjustable       Yes         Data areas and their retentivity       512 kbyte         Retentive data area (incl. timers, counters, flags), max.       512 kbyte         Flag       .         • Number, max.       16 kbyte         • Number of clock memories       8; 8 clock memory bit, grouped into one clock memory byte         Data blocks       .         • Retentivity adjustable       Yes         • Retentivity preset       No         Local data       .         • per priority class, max.       64 kbyte; max. 16 KB per block         Address area       .         Number of IO modules       4 096; max. number of modules / submodules         I/O address area       .         • Inputs	– adjustable	Yes
Retentivity       Yes         Data areas and their retentivity       512 kbyte         Retentive data area (incl. timers, counters, flags), max.       512 kbyte         Flag       512 kbyte         • Number, max.       16 kbyte         • Number of clock memories       8; 8 clock memory bit, grouped into one clock memory byte         Data blocks       8; 8 clock memory byte         • Retentivity adjustable       Yes         • Retentivity preset       No         Local data       64 kbyte; max. 16 KB per block         Address area       Number of IO modules         // O address area       32 kbyte; All outputs are in the process image         per integrated IO subsystem       32 kbyte         - Inputs       32 kbyte         - Outputs (volume)       8 kbyte         - Outputs (volume)       8 kbyte         - Outputs (volume)       8 kbyte         - Number of subprocess images, max.       32	IEC timer	
- adjustable       Yes         Data areas and their retentivity       512 kbyte         Retentive data area (incl. timers, counters, flags), max.       512 kbyte         Flag       16 kbyte         • Number, max.       16 kbyte         • Number of clock memories       8; 8 clock memory bit, grouped into one clock memory byte         Data blocks       -         • Retentivity adjustable       Yes         • Retentivity preset       No         Local data       64 kbyte; max. 16 KB per block         Address area       -         Number of IO modules       4 096; max. number of modules / submodules         I/O address area       -         • Inputs       32 kbyte; All inputs are in the process image         • Outputs       32 kbyte; All outputs are in the process image         per integrated IO subsystem       -         - Outputs (volume)       8 kbyte         - Outputs (volume)       8 kbyte         - Outputs (volume)       8 kbyte         - Outputs (volume)       32         • Number of subprocess images, max.       32         • Number of subprocess images, max.       32	Number	Any (only limited by the main memory)
Data areas and their retentivity         Retentive data area (incl. timers, counters, flags), max.         Flag         • Number, max.         • Number of clock memories         8; 8 clock memory bit, grouped into one clock memory byte         Data blocks         • Retentivity adjustable         • Retentivity preset         No         Local data         • per priority class, max.         64 kbyte; max. 16 KB per block         Address area         Number of IO modules         4 096; max. number of modules / submodules         I/O address area         • Inputs         • Outputs         per integrated IO subsystem         - Inputs (volume)         - Outputs (volume)         8 kbyte         Subprocess images         • Number of IO Controllers	Retentivity	
Retentive data area (incl. timers, counters, flags), max.       512 kbyte         Flag <ul> <li>Number, max.</li> <li>6 kbyte</li> <li>8; 8 clock memory bit, grouped into one clock memory byte</li> </ul> Data blocks <ul> <li>Retentivity adjustable</li> <li>Yes</li> <li>Retentivity preset</li> <li>No</li> </ul> Local data <ul> <li>per priority class, max.</li> <li>64 kbyte; max. 16 KB per block</li> </ul> Address area       4 096; max. number of modules / submodules               I/O address area <li>Inputs</li> <li>Outputs</li> <li>S 2 kbyte; All inputs are in the process image</li> <li>Per integrated IO subsystem                 <ul> <li>Inputs (volume)</li> <li>8 kbyte</li> <li>Subprocess images</li> <li>Number of subprocess images, max.</li> </ul>          Subprocess images       32         Number of IO Controllers       32</li>	– adjustable	Yes
flags), max.       If kbyte         Flag       16 kbyte         • Number of clock memories       8; 8 clock memory bit, grouped into one clock memory byte         Data blocks       *         • Retentivity adjustable       Yes         • Retentivity preset       No         Local data       64 kbyte; max. 16 KB per block         Address area       4 096; max. number of modules / submodules         I/O address area       32 kbyte; All inputs are in the process image         • Outputs       32 kbyte; All outputs are in the process image         • Inputs       32 kbyte; All outputs are in the process image         • Outputs       8 kbyte         Subprocess images       8 kbyte         • Number of subprocess images, max.       32		
Flag       16 kbyte         • Number, max.       16 kbyte         • Number of clock memories       8; 8 clock memory bit, grouped into one clock memory byte         Data blocks       Yes         • Retentivity adjustable       Yes         • Retentivity preset       No         Local data       64 kbyte; max. 16 KB per block         • per priority class, max.       64 kbyte; max. 16 KB per block         Address area       4 096; max. number of modules / submodules         I/O address area       32 kbyte; All inputs are in the process image         • Outputs       32 kbyte; All outputs are in the process image         per integrated IO subsystem       8 kbyte         - Unputs (volume)       8 kbyte         Subprocess images       32         • Number of subprocess images, max.       32		512 kbyte
<ul> <li>Number, max.</li> <li>Number of clock memories</li> <li>8; 8 clock memory bit, grouped into one clock memory byte</li> <li>Data blocks         <ul> <li>Retentivity adjustable</li> <li>Retentivity preset</li> <li>No</li> </ul> </li> <li>Local data         <ul> <li>per priority class, max.</li> <li>64 kbyte; max. 16 KB per block</li> </ul> </li> <li>Address area         <ul> <li>Number of IO modules</li> <li>4 096; max. number of modules / submodules</li> <li>I/O address area             <ul> <li>Inputs</li> <li>S2 kbyte; All inputs are in the process image</li> <li>Outputs (volume)</li> <li>8 kbyte</li> <li>Subprocess images                  <ul> <li>Number of IO Controllers</li> </ul> </li> </ul> </li> </ul></li></ul>		
• Number of clock memories       8; 8 clock memory bit, grouped into one clock memory byte         Data blocks       Yes         • Retentivity adjustable       Yes         • Retentivity preset       No         Local data       64 kbyte; max. 16 KB per block         Address area       4 096; max. number of modules / submodules         I/O address area       32 kbyte; All inputs are in the process image         • Inputs       32 kbyte; All outputs are in the process image         per integrated IO subsystem       8 kbyte         - Outputs (volume)       8 kbyte         Subprocess images       32         • Number of subprocess images, max.       32	-	16 kbyte
<ul> <li>Retentivity adjustable</li> <li>Retentivity preset</li> <li>No</li> <li>Local data         <ul> <li>per priority class, max.</li> <li>64 kbyte; max. 16 KB per block</li> </ul> </li> <li>Address area         <ul> <li>Number of IO modules</li> <li>4 096; max. number of modules / submodules</li> </ul> </li> <li>I/O address area         <ul> <li>Inputs</li> <li>Qutputs</li> <li>Outputs</li> <li>Outputs (volume)</li> <li>A kbyte</li> <li>Outputs (volume)</li> <li>Kbyte</li> </ul> </li> <li>Subprocess images         <ul> <li>Number of subprocess images, max.</li> </ul> </li> <li>Subprocess images</li> <li>Number of IO Controllers</li> </ul>	Number of clock memories	
<ul> <li>Retentivity preset</li> <li>Retentivity preset</li> <li>No</li> <li>Local data         <ul> <li>per priority class, max.</li> <li>64 kbyte; max. 16 KB per block</li> </ul> </li> <li>Address area         <ul> <li>Number of IO modules</li> <li>4 096; max. number of modules / submodules</li> </ul> </li> <li>I/O address area         <ul> <li>Inputs</li> <li>Subprocess images</li> <li>Number of subprocess images, max.</li> </ul> </li> <li>Number of IO Controllers</li> </ul>	Data blocks	
Local data       64 kbyte; max. 16 KB per block         Address area       64 kbyte; max. 16 KB per block         Number of IO modules       4 096; max. number of modules / submodules         I/O address area       32 kbyte; All inputs are in the process image         • Inputs       32 kbyte; All outputs are in the process image         per integrated IO subsystem       8 kbyte         - Inputs (volume)       8 kbyte         Subprocess images       32         • Number of IO Controllers       32	Retentivity adjustable	Yes
<ul> <li>per priority class, max.</li> <li>64 kbyte; max. 16 KB per block</li> <li>Address area         <ul> <li>Number of IO modules</li> <li>4 096; max. number of modules / submodules</li> </ul> </li> <li>I/O address area         <ul> <li>Inputs</li> <li>Outputs</li> <li>Outputs</li> <li>Inputs (volume)</li> <li>Kbyte</li> <li>Outputs (volume)</li> <li>8 kbyte</li> </ul> </li> <li>Subprocess images         <ul> <li>Number of IO Controllers</li> </ul> </li> </ul>	Retentivity preset	No
Address area       4 096; max. number of modules / submodules         I/O address area       32 kbyte; All inputs are in the process image         • Inputs       32 kbyte; All outputs are in the process image         • Outputs       32 kbyte; All outputs are in the process image         per integrated IO subsystem       8 kbyte         - Inputs (volume)       8 kbyte         Subprocess images       32         • Number of subprocess images, max.       32	Local data	
Number of IO modules       4 096; max. number of modules / submodules         I/O address area       32 kbyte; All inputs are in the process image         • Inputs       32 kbyte; All outputs are in the process image         • Outputs       32 kbyte; All outputs are in the process image         per integrated IO subsystem       8 kbyte;         - Inputs (volume)       8 kbyte         Subprocess images       32         • Number of subprocess images, max.       32	• per priority class, max.	64 kbyte; max. 16 KB per block
I/O address area       32 kbyte; All inputs are in the process image         • Inputs       32 kbyte; All outputs are in the process image         • Outputs       32 kbyte; All outputs are in the process image         per integrated IO subsystem       8 kbyte;         - Inputs (volume)       8 kbyte         - Outputs (volume)       8 kbyte         Subprocess images       32         • Number of subprocess images, max.       32	Address area	
<ul> <li>Inputs</li> <li>Outputs</li> <li>Outputs</li> <li>a 2 kbyte; All inputs are in the process image</li> <li>32 kbyte; All outputs are in the process image</li> <li>a 2 kbyte; All outputs are in the process image</li> <li>b kbyte; All outputs are in the process image</li> <li>a 1 nputs (volume)</li> <li>b kbyte</li> <l< td=""><td>Number of IO modules</td><td>4 096; max. number of modules / submodules</td></l<></ul>	Number of IO modules	4 096; max. number of modules / submodules
<ul> <li>Outputs</li> <li>Outputs</li> <li>32 kbyte; All outputs are in the process image</li> <li>Inputs (volume)</li> <li>Outputs (volume)</li> <li>8 kbyte</li> <li>8 kbyte</li> <li>8 kbyte</li> <li>32 kbyte</li> </ul>	I/O address area	
per integrated IO subsystem     8 kbyte       - Inputs (volume)     8 kbyte       - Outputs (volume)     8 kbyte       Subprocess images     32       • Number of subprocess images, max.     32	Inputs	32 kbyte; All inputs are in the process image
<ul> <li>Inputs (volume) 8 kbyte</li> <li>Outputs (volume) 8 kbyte</li> <li>Subprocess images         <ul> <li>Number of subprocess images, max.</li> </ul> </li> <li>32</li> </ul>	Outputs	32 kbyte; All outputs are in the process image
- Outputs (volume)     8 kbyte       Subprocess images     32       Number of IO Controllers     32	per integrated IO subsystem	
Subprocess images     32       • Number of subprocess images, max.     32       Number of IO Controllers     32	<ul> <li>Inputs (volume)</li> </ul>	8 kbyte
Number of subprocess images, max. 32  Number of IO Controllers	<ul> <li>Outputs (volume)</li> </ul>	8 kbyte
Number of IO Controllers	Subprocess images	
	Number of subprocess images, max.	32
• integrated 1	Number of IO Controllers	
	integrated	1

Article number	6ES7515-2RM00-0AB0
Time of day	
Clock	
Backup time	6 wk; At 40 °C ambient temperature, typically
• Deviation per day, max.	10 s; Typ.: 2 s
Operating hours counter	
Number	16
Clock synchronization	
supported	Yes
• in AS, master	No
• in AS, slave	No
on Ethernet via NTP	Yes
Interfaces	
Number of PROFINET interfaces	1
1. Interface	
Interface types	
Number of ports	2
<ul> <li>integrated switch</li> </ul>	Yes
• RJ 45 (Ethernet)	Yes; X1
Protocols	
IP protocol	Yes; IPv4
PROFINET IO Controller	Yes
PROFINET IO Device	No
SIMATIC communication	Yes; Only Server
Open IE communication	Yes
Web server	No
Media redundancy	Yes

Article number	6ES7515-2RM00-0AB0
PROFINET IO Controller	
Services	
<ul> <li>PG/OP communication</li> </ul>	Yes
<ul> <li>S7 routing</li> </ul>	No
<ul> <li>Isochronous mode</li> </ul>	No
<ul> <li>Open IE communication</li> </ul>	Yes
– IRT	No
– MRP	Yes; Only Manager Auto, max. 50 nodes; only 16 are recommended, however
– MRPD	No
<ul> <li>PROFlenergy</li> </ul>	Yes
<ul> <li>Number of connectable IO Devices, max.</li> </ul>	64
<ul> <li>Updating times</li> </ul>	The minimum value of the update time also de- pends on communication share set for PROFINET IO, on the number of IO devices, and on the quantity of configured user data
Update time for RT	
<ul> <li>for send cycle of 1 ms</li> </ul>	1 ms to 512 ms
2. Interface	
Interface types	1
Number of ports	1
integrated switch	No
RJ 45 (Ethernet)	Yes; X2
Protocols	Varia ID: 4
IP protocol	Yes; IPv4
PROFINET IO Controller	No
PROFINET IO Device	No
SIMATIC communication	Yes; Only Server
Open IE communication	Yes
Web server	No
Media redundancy	No
Interface types	
RJ 45 (Ethernet)	
• 100 Mbps	Yes
Autonegotiation	Yes
Autocrossing	Yes
Industrial Ethernet status LED	Yes

Article	e number	6ES7515-2RM00-0AB0
Proto	cols	
Numb	per of connections	-
•	Number of connections, max.	108
•	Number of connections reserved for ES/HMI/web	10
Redu	ndancy mode	
•	MRP	Yes; Manager Auto is permanently set in TIA. Max. 50 nodes are possible, 16 are recommend- ed
•	MRPD	No
SIMA	TIC communication	
•	S7 communication, as server	Yes
•	S7 communication, as client	No
Open	IE communication	
•	TCP/IP	Yes
	<ul> <li>Data length, max.</li> </ul>	64 kbyte
	<ul> <li>several passive connections per port, supported</li> </ul>	Yes
•	ISO-on-TCP (RFC1006)	Yes
	<ul> <li>Data length, max.</li> </ul>	64 kbyte
•	UDP	Yes
	<ul> <li>Data length, max.</li> </ul>	2 kbyte; 1 472 bytes for UDP broadcast
	<ul> <li>UDP multicast</li> </ul>	Yes; Max. 5 multicast circuits
•	DHCP	No
•	SNMP	Yes
•	DCP	Yes
•	LLDP	Yes
	server	
•	HTTP	No
•	HTTPS	No
OPC	UA	
٠	OPC UA client	No
•	OPC UA server	No
Furth	er protocols	
•	MODBUS	Yes; MODBUS TCP
Media	a redundancy	
•	Switchover time on line break, typ.	200 ms; PROFINET MRP
•	Number of stations in the ring, max.	50; Only 16 are recommended, however

Article number	6ES7515-2RM00-0AB0
S7 message functions	
Program alarms	No
Test commissioning functions	
Joint commission (Team Engineering)	No
Status block	Yes; up to 8 simultaneously
Single step	No
Status/control     Status/control variable	Yes
Variables	Inputs/outputs, memory bits, DBs, distributed I/Os, timers, counters
Number of variables, max.	
<ul> <li>of which status variables, max.</li> </ul>	200; per job
<ul> <li>of which control variables, max.</li> </ul>	200; per job
Forcing	
Forcing, variables	Peripheral inputs/outputs
Number of variables, max.	200
Diagnostic buffer	
• present	Yes
• Number of entries, max.	3 200
<ul> <li>of which powerfail-proof</li> </ul>	500
Traces	
Number of configurable Traces	4
• Memory size per trace, max.	512 kbyte
Interrupts/diagnostics/status information	
Diagnostics indication LED	
RUN/STOP LED	Yes
ERROR LED	Yes
MAINT LED	Yes
Connection display LINK TX/RX	Yes
Supported technology objects	
Motion Control	No
Controller	
PID_Compact	No
PID_3Step	No
PID-Temp	No
Counting and measuring	
High-speed counter	No

Article number	6ES7515-2RM00-0AB0
Standards, approvals, certificates	
Suitable for safety functions	No
Ambient conditions	
Ambient temperature during operation	
horizontal installation, min.	0°C
horizontal installation, max.	60 °C; Display: 50 °C, at an operating tempera- ture of typically 50 °C, the display is switched off
vertical installation, min.	0 °C
• vertical installation, max.	40 °C; Display: 40 °C, at an operating tempera- ture of typically 40 °C, the display is switched off
Ambient temperature during stor- age/transportation	
• min.	-40 °C
• max.	70 °C
Configuration	
Programming	
Programming language	
– LAD	Yes
– FBD	Yes
– STL	Yes
– SCL	Yes
– CFC	No
– GRAPH	No
Know-how protection	
User program protection/password protec- tion	Yes
Copy protection	No
Block protection	Yes
Access protection	
Password for display	Yes
Protection level: Write protection	Yes
Protection level: Read/write protection	Yes
Protection level: Complete protection	Yes

Article number	6ES7515-2RM00-0AB0
Dimensions	
Width	70 mm
Height	147 mm
Depth	129 mm
Weights	
Weight, approx.	830 g

You can find information on the general technical specifications, such as standards and approvals, electromagnetic compatibility, protection class, etc. in the system manual for Redundant System S7-1500R/H.

### See also

S7-1500R/H Redundant System (https://support.industry.siemens.com/cs/ww/en/view/109754833)

# **Dimension drawing**



### A.1 Dimension drawing

This section contains the dimensional drawing of the module on the mounting rail, as well as a dimensional drawing with the front panel open. Keep to the dimensions when installing in cabinets, control rooms, etc.

### Dimension drawings of the CPU 1515R-2 PN

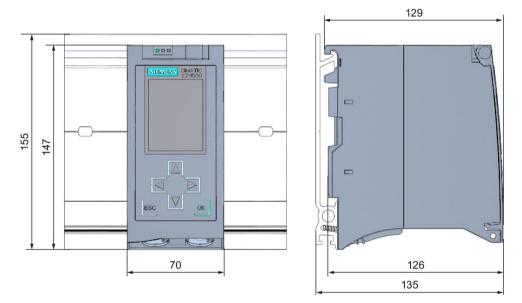


Figure A-1 Dimension drawing of the CPU 1515R-2 PN, front and side view

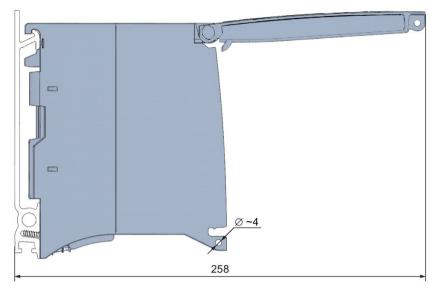


Figure A-2 Dimension drawing CPU 1515R-2 PN, side view with open front panel