SIEMENS

Press

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Siemens to expand portfolio for semiconductor design and verification with the planned acquisition of Lauterbach

- **Planned acquisition will enable Siemens to integrate hardware-assisted verification with embedded software debug and trace technology into a single workflow**
- **Combined workflow to empower customers to identify hardware and software issues earlier, boost engineering productivity, and accelerate time-to-market**

Siemens has signed an agreement to acquire Lauterbach, a leading provider of debug and trace solutions for embedded software and complex system-on-chip (SoC) architectures. Lauterbach develops comprehensive, modular and extensible development and verification tools for microprocessors and embedded CPU cores in the form of hardware-assisted debuggers and real-time trace tools. The combination of Siemens' and Lauterbach's complementary technologies enables a unified view of chip and software behavior and helps customers more quickly identify errors arising from complex interactions between hardware and software during chip development. The results are faster time to revenue and higher product quality.

The planned acquisition represents another important step in further establishing Siemens' position as one of the world's leading electronic design automation (EDA) powerhouses. By adding industry-leading debugging technologies to its hardware-assisted verification solutions, Siemens is further extending its competitive edge.

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The planned combined solution will enable customers to develop and verify embedded software even earlier, and well before the physical chip is available.

By bringing together these previously separate workflows into a single solution Siemens aims to empower customers with a unified hardware/software view of how increasingly complex application-specific integrated circuit (ASIC) and system-on-a-chip (SoC) devices behave. This shift-left approach helps customers analyze hardware/software interactions at an early stage and significantly shortens the time-to-market for new software-defined products.

“With Lauterbach, we are further extending our industry-leading EDA portfolio for semiconductor design by bringing together our premier hardware-assisted verification portfolio with its industry-leading software debugging technologies. As semiconductors represent a core vertical for our technology strategy, this enables customers to test and optimize their products even before the actual chip is produced – significantly reducing development time,” said Cedrik Neike, Member of the Managing Board of Siemens AG and President Digital Industries.
“Lauterbach is a remarkable success story, written in Germany for the global semiconductor industry. We will build on the impressive legacy that founders Lothar and Stephan Lauterbach and their team have achieved over the past decades and are very pleased to welcome the Lauterbach group to Siemens. We are also delighted that Stephan Lauterbach and Thomas Ullmann will continue their work as part of Siemens following the closing of the transaction.”

Lauterbach and Siemens solutions are already used by numerous mutual customers. The increasing complexity of modern semiconductors and embedded software, driven by trends such as artificial intelligence, software-defined vehicles, Internet of Things (IoT), multi-core architectures, cybersecurity and functional safety, is leading to greater convergence between hardware and software development. On that basis, Siemens expects significant revenue synergies from the transaction.

Headquartered close to Munich, Germany, Lauterbach was co-founded by the brothers Lothar and Stephan Lauterbach beginning in 1979 to develop tools for microprocessor development in the form of hardware-assisted debuggers and real-time trace tools. Lauterbach employs around 170 people and serves more than

1,200 global customers across various industries, including leaders and innovators in automotive, semiconductors and telecommunications. In 2025, the company generated roughly € 60 million in revenues.

“The increasing convergence of hardware and software development is fundamentally changing the industry and our customers’ requirements,” said Stephan Lauterbach, CTO and co-founder of Lauterbach. “By combining our debug and trace technology with Siemens’ hardware-assisted verification capabilities, we are creating a powerful solution that provides developers with a unified hardware/software view of the behavior of increasingly complex ASIC and SoC devices in AI, high-performance computing, storage/hyperscale, automotive, communications and A&D applications. Our commitment to keeping our customers at the center of everything we do remains unchanged.”

The transaction is expected to close in the first half of 2027, subject to customary closing conditions and required regulatory approvals.

The financial terms of the transaction will not be disclosed.

This press release is available at https://sie.ag/28q5nA.

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In fiscal 2025, which ended on September 30, 2025, the Siemens Group generated revenue of €78.9 billion and net income of €10.4 billion. As of September 30, 2025, the company employed around 318,000 people worldwide on the basis of continuing operations. Further information is available on the Internet at www.siemens.com.

Lauterbach is a leading manufacturer of cutting-edge development tools for embedded systems, with more than 45 years of experience. It is an international, well-established company that serves customers all over the world, partners with all major semiconductor manufacturers and continues to grow steadily. At its headquarters in Hoehenkirchen near Munich, the engineering team develops and produces highly capable, specialized and easy-to-use development tools under the TRACE32® brand. Branch offices in the United Kingdom, Italy, France, Tunisia, on the East and West Coast of the United States, Japan and China, along with highly qualified sales and support engineers in many other countries, make Lauterbach's full product range available worldwide.
For more information, please visit www.lauterbach.com.

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